

FIG. 1

ANODE SIDE	THICKNESS(nm)	CATHODE SIDE	THICKNESS(nm)
0 LAYER(LIGHT EMITTING LAYER)	0	0 LAYER(LIGHT EMITTING LAYER)	0
1st (HTL / HIL)	33	1st LAYER(ETL / EIL)	90
2nd(ITO)	110	2ndLAYER(Ag)	8
3rd(SP-SiN)	100	3rdLAYER(ITO)	260
4th(ACRYLIC RESIN)	1000	4thLAYER(SP-SiN)	100
5th(SiO <sub>2</sub> )	100	5thLAYER(AIR)	0
6th(GI)	110	GLASS SUBSTRATE	
7th(SiO <sub>2</sub> )	100		
8th(SiO <sub>2</sub> )	50		
GLASS SUBSTRATE			



## LAMINATED STRUCTURE

ANODE LAYER TFT \ ITO \ EL(HIL \ HTL \ EML) \ EL(EML \ ETL \ EIL) \ Ag \ ITO \ SP-SiN CATHODE LAYER



FIG. 2

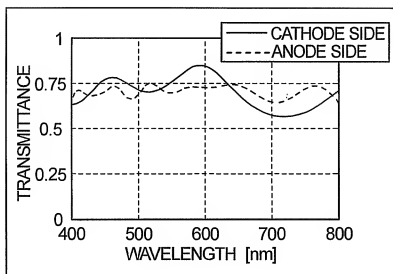


FIG. 3A

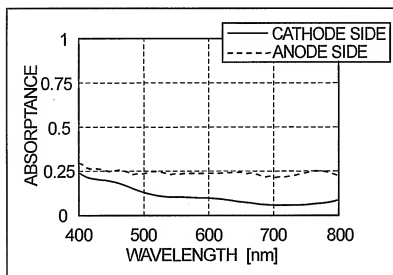


FIG. 3B

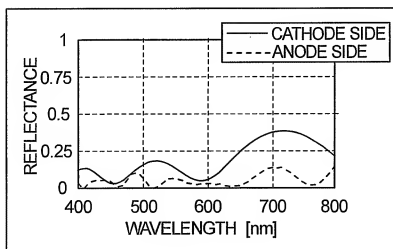


FIG. 3C

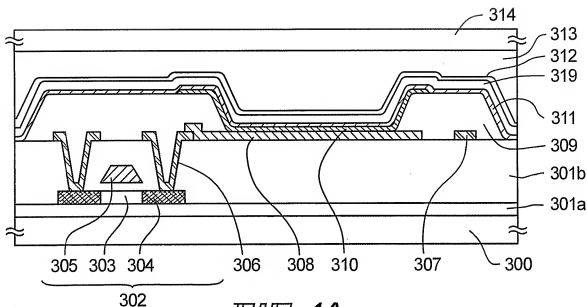


FIG. 4A

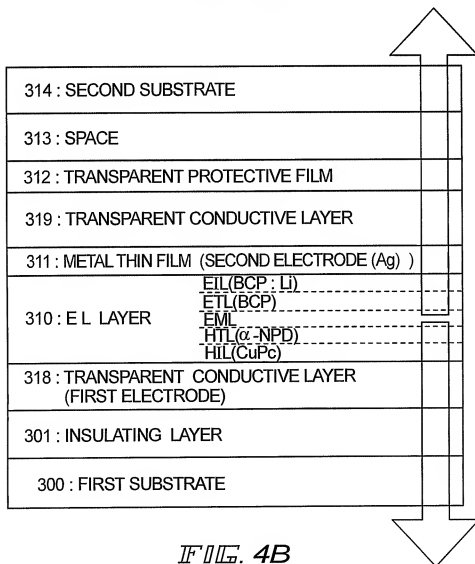


FIG. 4B

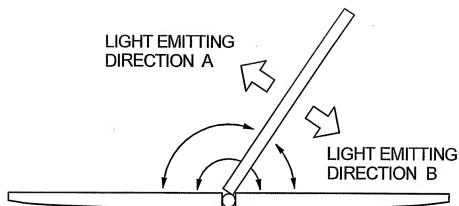


FIG. 5A

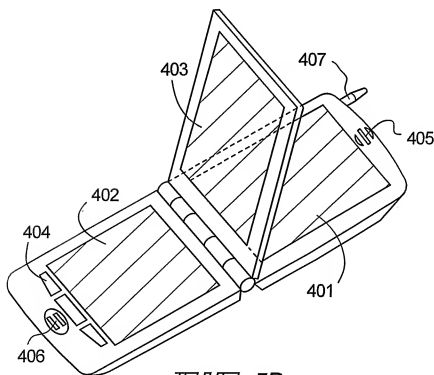


FIG. 5B

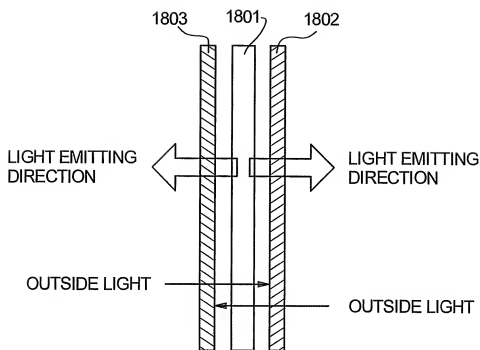


FIG. 6A

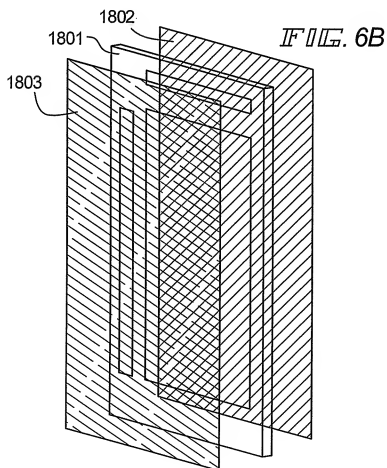


FIG. 6B

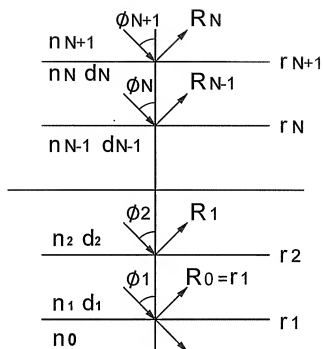


FIG. 7

FIG. 8

